

#### **TVS/ESD** Arrays

## RLSD32A121VM

## Description

Designed to protect voltage sensitive electronic components from ESD and other transients. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD.

The combination of small size, high level of ESD protection makes them a flexible solution for applications such as Digital cameras, cellular phones, and MP3 Players. It is designed to replace multiplayer varistors (MLV) in consumer equipments applications such as mobile phone, notebook, PAD, STB, LCD TV etc.

#### Features

- Uni-directional ESD protection of one line
- Reverse stand-off voltage: 12V
- Low reverse clamping voltage
- Low leakage current
- Excellent package:1.70mm×1.30mm×1.00mm
- Fast response time
- JESD22-A114-B ESD Rating of class 3B per human body model
- IEC 61000-4-2 Level 4 ESD protection

#### Applications

- Computers and peripherals
- Digital Cameras
- Audio and video equipment
- Cellular handsets and accessories
- Portable electronics
- Mp3 Players
- Other electronics equipments communication systems
- Smart Phones
- Laptop Computers
- Portable Electronics



# Pin Configuration

**Circuit Diagram** 



Front side

## Part Number Code









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#### Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp =8/20µs)	P <sub>PP</sub>	800	W
ESD Voltage (Contact)	VESD	±30	kV
ESD Voltage (Air)	V <sub>ESD</sub>	±30	kV
Lead Soldering Temperature	ΤL	260 (10 s)	°C
Junction Temperature	Tj	150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C

#### Electrical Characteristics (@ 25°C Unless Otherwise Specified)

	Reverse Stand-Off Voltage	Minimum Breakdown Voltage	Peak Pulse Voltage @8/20μS	Peak Pulse Current @8/20µS	Reverse Leakage @V <sub>RWM</sub>	Max Capacitance
Type Number	V <sub>RWM</sub>	V <sub>BR</sub> @1mA	Vc@1A	Ірр	I <sub>R</sub> @V <sub>RWM</sub>	DC=0V CJ@ 1 MHz
	v	v	v	A	μA	pF
RLSD32A121VM	12	13.3	15	40	1.0	150

#### Electrical Parameters (T=25°C)

Symbol	Parameter		
I <sub>pp</sub>	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ Ipp		
V <sub>RWM</sub>	Working Peak Reverse Voltage		
IR	Maximum Reverse Leakage Current @ V <sub>RWM</sub>		
V <sub>BR</sub>	Breakdown Voltage @ I <sub>T</sub>		
Ιτ	Test Current		
lF	Forward Current		
VF	Forward Voltage @ I <sub>F</sub>		



# **Characteristic Curves**



# **361°**Circuit Protection System

Fig2.ESD Pulse Waveform (according to IEC61000-4-2)







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# Dimensions & Recommended soldering footprint(mm)



#### Soldering Parameters - Reflow Soldering (Surface Mount Devices)



Reflow Condition		Pb - Free assembly	
Pre Heat	-Temperature Min (T <sub>s(min)</sub> )	150°C	
	-Temperature Max (T <sub>s(max)</sub> )	200°C	
	- Time (min to max) ( $t_s$ )	60 -180 Seconds	
Average ramp up rate ( Liquids Temp T <sub>L</sub> ) to peak		3°C/second max	
T <sub>S(max)</sub> to TL - Ramp-up Rate		3°C/second max	
Reflo w	- Temperature (T <sub>L</sub> ) (Liquids)	217°C	
	- Time (min to max) ( $t_s$ )	60 -150 Seconds	
Peak Temperature (T <sub>P</sub> )		260 +0/-5°C	
Time within 5°C of actual peak Temperature (t <sub>p</sub> )		20 - 40 Seconds	
Ramp-down Rate		6°C/second max	
Time 25°C to peak Temperature (T <sub>P</sub> )		8 minutes Max	
Do not exceed		280°C	



